ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention

EFS ID:

METHOD FOR DESIGNING AN INTEGRATED CIRCUIT HAVING MULTIPLE VOLTAGE DOMAINS

Submision Type :

Utility Patent Filing

Application Number:

10/707068 50883

Server Response:

Confirmation Code	Message
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WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font fomat.</invention-title></invention-title>

First Named Applicant:

Joseph ladanza

Attorney Docket Number:

BUR920030126US1

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2003-11-19 11:26:44 EDT

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